Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 45565	SERIAL NO. 10/634,746
	INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	APPLICANT Sung-Jin Park et al.	
		FILING DATE August 6, 2003	GROUP

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

PUBLISHED U.S. PATENT APPLICATIONS

		Pub. No.	Pub. Date	Patentee	CLASS	SUB-CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	NO
F.P.		1	1	2	2	8	9	0	8/2002	European	H03M	13/39	X	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

F.P.		Mohammad M. Mansour et al., "Design Methodology for High-Speed Iterative Decoder Architectures", <u>Proceeding of IEEE Conference on Acoustics, Speech, and Signal Processing</u>, Vol. 3, pages III-3085 – III-3088 (May 2002).
		Alexander Worm et al., "VLSI Architectures for High-Speed MAP Decoders", <u>Proceeding of 14th International Conference on VLSI Design</u>, pages 446-453 (January 2001).

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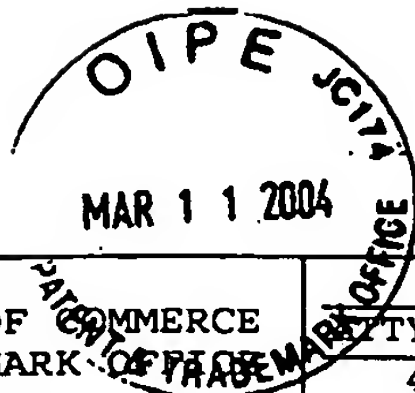
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		Pub. No.	Pub. Date	Patentee	CLASS	SUB-CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	NO
F.P.		1	2	6	1	1	3	9	11/2002	EP	H03M	13/29		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

F.P.	"Biodirectonal Shift Registers", HTTP://WWW.EELAB.USYD.EDU.AU/DIGITAL TUTORIAL/PART2/REGISTER06.HTML, (February 1999)
	Guido Masera et al., "VLSI Architectures for Turbo Codes", IEEE Inc., Vol. 7, No. 3, pages 369-379 (Sept. 1999)
	Warren J. Gross et al., "Difference Metric Soft-Output Detection: Architecture and Implementation", pages 1-8 (Sept. 2001).
	N. Sklavos et al., "A New Low Power and High Speed Didirectional Shift Register Architecture", VLSI Design Laboratory, (July 2001)

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PTO-1449 U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO. 45565	SERIAL NO. 10/634,746
APPLICANT Sun-Jin Park , et al.	
FILING DATE August 6, 2003	GROUP Not Assigned

U.S. Patent Documents

		Patent No.	Date	Patentee	Class	Sub-class	Filing Date

Published U.S. Patent Application

		Patent No.	Pub. Date	Patentee	Class	Sub-class	Filing Date

Foreign Patent Documents or Foreign Patent Applications

		Document No.	Pub. Date	Country or Patent Office	Class	Sub-class	English - Abstract Yes	No
F.P.		1261139	11-2002	EPO			X	

Other Documents (including Arthur, Title, Date, Pertinent Pages, etc.)

F.P.		Gross, Warren J., Gaudet, Vincent C., Gulak, P. Glenn, "Difference Metric Soft-Output Detection: Architecture and Implementation" pp. 1-8, figure 7, September 2001.
		Sklavos, N., Kitsos, P., Zervas, N., & Koufopavlou, O.; "A New Low Power and high Speed Bidirectional Shift Register Architecture", figures 1 and 2, July 2001.
		Masera, G., et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, IEEE Inc., New York, vol. 7, no. 3, pp. 369-379, September 1999.
		"Bidirectional Shift Registers", February 1999.

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